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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/613,803	07/03/2003	Takae Ito	2803.68136	7339
7590	12/14/2005		EXAMINER	
Patrick G. Burns, Esq. GREER, BURNS & CRAIN, LTD. Suite 2500 300 South Wacker Dr. Chicago, IL 60606			VU, PHU	
			ART UNIT	PAPER NUMBER
			2871	
			DATE MAILED: 12/14/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.	10/613,803	Applicant(s)	ITO, TAKAE
Examiner	Phu Vu	Art Unit	2871

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on _____.
2a) This action is FINAL. 2b) This action is non-final.
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1, 3-5 is/are pending in the application.
4a) Of the above claim(s) ____ is/are withdrawn from consideration.
5) Claim(s) ____ is/are allowed.
6) Claim(s) 1 and 3-5 is/are rejected.
7) Claim(s) ____ is/are objected to.
8) Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
10) The drawing(s) filed on 03 July 2003 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
 Paper No(s)/Mail Date _____.
4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____.
5) Notice of Informal Patent Application (PTO-152)
6) Other: _____.

DETAILED ACTION

Response to Arguments

Applicant's arguments filed have been fully considered but they are not persuasive. Applicant states that Sato applies to a different art than liquid crystal therefore would not be obvious to combine. However the limitation of driving circuitry is shared by any device using integrated circuits as the same as driving circuitry applied to printers for example can be applied to liquid crystal since there is no apparent structure (ie wirings, signals, driver ICs) that is limited to purely liquid crystal displays.

A new grounds of rejection was also made in response of the amendment.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-3 are rejected under 35 U.S.C. 103(a) as being obvious over Oishi 6532055 in view of Sato et. al. US Patent No. 6345887.

Oishi teaches a liquid crystal display panel displaying an image; a PCB arranged along one side of the LCD display panel (see fig. 2A and 2B elements 3 and 4); and a plurality of flexible circuit boards which connect the liquid crystal panel to the PCB, each driver IC (18 and 25) outputting a liquid crystal driving signal to drive the liquid crystal of

the liquid crystal panel, wherein the PCB includes a wiring system so arranged that a bundle of main wirings (VCC, GND, CLOCK) which receive an input signal, have plural bundles of branch wirings outputting the input signal to one of the driver ICs mounted on the flexible circuit board and the driver ICs in each flexible circuit board are cascaded (start signal) to each other each other with respect to the input signal.

Oishi fails to teach two driver ICs that are mounted on each flexible printed circuit board. Sato teaches two driver ICs (fig. 2 element 15) mounted on a single printed circuit board (see fig. 2 element 12). Sato's driver ICs are not cascaded however the primary reference teaches the driver ICs are cascaded with the respect to the inputs as the inputs 16, 17 and 18 of figure 1B are cascaded. Multiple ICs are patterned on a single circuit board allow for faster production as multiple ICs can be mounted at one time, or reduce the amount of cutting required on the PCB, or reduce the total amount area of the PCB needed. Therefore, at the time of the invention, it would have been obvious to include two driver ICs on a single PCB to increase production speed or reduce the amount of materials needed.

Regarding claim 3, the primary reference each of the flexible printed circuit boards has input terminals which has same number of that one IC (see figs 2A and 2B).

Regarding claim 4, Oishi teaches the internal wirings are on aside of the PCB with respect to the driver ICs (see figs 2A and 2B).

Regarding claim 5, Oishi teaches the bundles of branch wirings are branched from the main wirings at positions different from one another (see figs. 2A and 2B).

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

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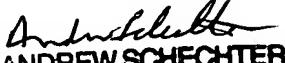
mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phu Vu whose telephone number is (571)-272-1562. The examiner can normally be reached on 8AM-5PM M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571)-272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Phu Vu
Examiner
AU 2871


ANDREW SCHECHTER
PRIMARY EXAMINER